## UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 6,979,068 B2

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DATED

: December 27, 2005

INVENTOR(S) : Daigoro Kanematsu

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

## Column 2,

Line 37, replace "Publish d" with -- Published --.

## Column 3,

Line 21, replace "th" with -- the --.

## Column 9,

Line 39, replace "(.g, CPU, MPU)" with -- (e.g., CPU, MPU) --; and Line 63, replace "cod" with -- code --.

Signed and Sealed this

Twenty-eighth Day of March, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office